

To make effective use of high performance CMOS cameras.

CIS Corporation
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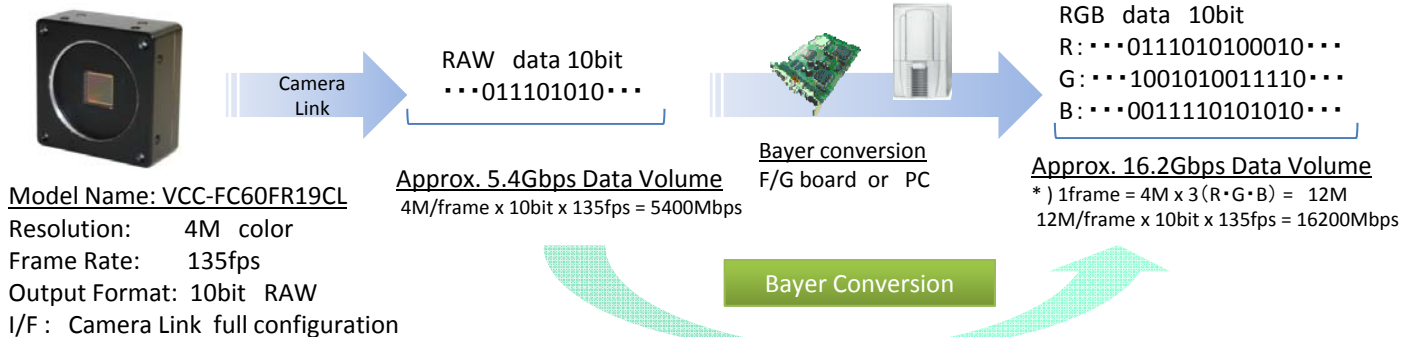
First of All

Technical innovation of CMOS sensor is making a developmental leap and its field of application has been broadening rapidly. CIS enhances product lineups with high performances such as high speed and high resolution, taking advantages of utilization of CMOS sensor.

To make effective use of high performance CMOS cameras however, application system configuration shall be fully considered. Here are some verification points to be concerned on the amount of transferring data.

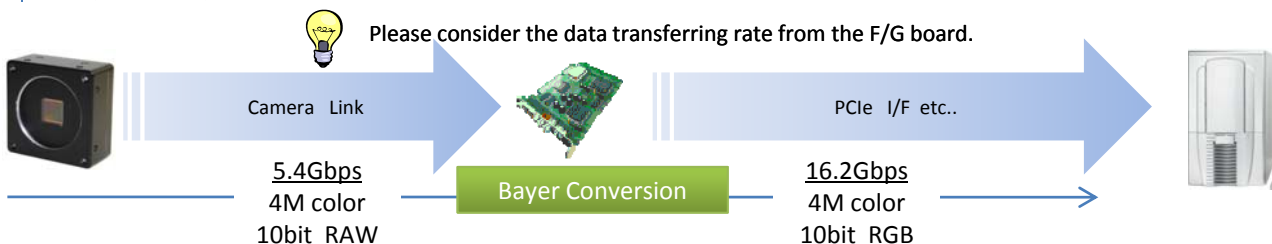
Example: VCC-FC60FR19CL

In case of CIS 4M color camera, the camera output data volume will be approx. 5.4Gbps. The data output from the camera is 10bit RAW so that Bayer conversion process shall be done by the F/G or the PC. The data volume after completion of Bayer conversion process will be very large, approx. 16.2Gbps.



Bayer Conversion with F/G board

When PCIe is used as the interface between the frame grabber board and the PC, the data transferring rate per 1 lane will be 2.5Gbps (theoretical value), and there are possibilities that sufficient frame rate can not be acquired. Large-capacity interface such as PCIe x 8 would be needed.



Bayer Conversion with PC

Bayer conversion from RAW 10 bit to RGB 10 bit is CPU-intensive so that there are possibilities that sufficient frame rate can not be acquired. Countermeasures to reduce loads on CPU, such as splitting Bayer conversion by multi-core, shall be taken.

